

WHAT IS CLAIMED IS:

- 1 1. A method for debugging software code, said method
2 comprising:
3 initiating a debugger thread on a computer system,
4 wherein the debugger thread performs a plurality of
5 debugger events;
6 invoking an operational thread on the computer system
7 using the debugger thread, the operational thread
8 performing operational tasks;
9 executing the software code using the operational
10 thread;
11 detecting whether the operational thread is
12 functioning using the debugger thread; and
13 debugging the operational thread using the debugger
14 thread in response to the detecting.
- 1 2. The method as described in claim 1 further comprising:
2 receiving a request from a software developer;
3 retrieving operational data from the operational
4 thread using the debugger thread in response to the
5 request; and
6 providing the operational data to the developer.
- 1 3. The method as described in claim 2 wherein the
2 operational data is selected from the group consisting
3 of a register value, a code line failure value, an
4 interrupt value, and a memory value.

- 1 4. The method as described in claim 2 wherein the
2 operational data is one or more register values, and
3 wherein the register locations are different from
4 debugger register locations that are used by the
5 debugger thread.
- 1 5. The method as described in claim 1 further comprising:
2 loading a debugger operating system on the debugger
3 thread; and
4 loading a primary operating system on the operational
5 thread, wherein the debugger operating system is
6 different from the primary operating system.
- 1 6. The method as described in claim 1 wherein the
2 computer system includes a plurality of dissimilar
3 processors, wherein the operational thread and the
4 debugger thread both execute on a common dissimilar
5 processor from the plurality of dissimilar processors.
- 1 7. The method as described in claim 2 wherein the
2 dissimilar processor is a processing unit.
- 1 8. An information handling system comprising:
2 a processor;
3 a memory accessible by the processor;
4 one or more nonvolatile storage devices accessible by
5 the processor; and
6 a debugger thread tool for debugging an operational
7 thread, the debugger thread tool comprising software
8 code effective to:

9 initiate a debugger thread on a computer
10 system, wherein the debugger thread performs
11 a plurality of debugger events;

12 invoke an operational thread on the computer
13 system using the debugger thread, the
14 operational thread performing operational
15 tasks;

16 execute the software code using the
17 operational thread;

18 detect whether the operational thread is
19 functioning using the debugger thread; and

20 debug the operational thread using the
21 debugger thread in response to the
22 detecting.

1 9. The information handling system as described in claim
2 8 wherein the software code is further effective to:
3 receive a request from a software developer;

4 retrieve operational data from the operational thread
5 using the debugger thread in response to the request;
6 and

7 provide the operational data to the developer.

1 10. The information handling system as described in claim
2 9 wherein the operational data is selected from the
3 group consisting of a register value, a code line
4 failure value, an interrupt value, and a memory value.

1 11. The information handling system as described in claim
2 9 wherein the operational data is one or more register

3 values, and wherein the register locations are
4 different from debugger register locations that are
5 used by the debugger thread.

1 12. The information handling system as described in claim
2 8 wherein the software code is further effective to:
3 load a debugger operating system on the debugger
4 thread; and
5 load a primary operating system on the operational
6 thread, wherein the debugger operating system is
7 different from the primary operating system.

1 13. The information handling system as described in claim
2 8 wherein the computer system includes a plurality of
3 dissimilar processors, wherein the operational thread
4 and the debugger thread both execute on a common
5 dissimilar processor from the plurality of dissimilar
6 processors.

1 14. A computer program product stored on a computer
2 operable media for debugging an operational thread
3 using a debugger thread, said computer program product
4 comprising:
5 means for initiating a debugger thread on a computer
6 system, wherein the debugger thread performs a
7 plurality of debugger events;
8 means for invoking an operational thread on the
9 computer system using the debugger thread, the
10 operational thread performing operational tasks;
11 means for executing the software code using the
12 operational thread;

13 means for detecting whether the operational thread is
14 functioning using the debugger thread; and
15 means for debugging the operational thread using the
16 debugger thread in response to the detecting.

1 15. The computer program product as described in claim 14
2 further comprising:
3 means for receiving a request from a software
4 developer;
5 means for retrieving operational data from the
6 operational thread using the debugger thread in
7 response to the request; and
8 means for providing the operational data to the
9 developer.

1 16. The computer program product as described in claim 15
2 wherein the operational data is selected from the
3 group consisting of a register value, a code line
4 failure value, an interrupt value, and a memory value.

1 17. The computer program product as described in claim 15
2 wherein the operational data is one or more register
3 values, and wherein the register locations are
4 different from debugger register locations that are
5 used by the debugger thread.

1 18. The computer program product as described in claim 14
2 further comprising:
3 means for loading a debugger operating system on the
4 debugger thread; and

5 means for loading a primary operating system on the
6 operational thread, wherein the debugger operating
7 system is different from the primary operating system.

1 19. The computer program product as described in claim 14
2 wherein the computer system includes a plurality of
3 dissimilar processors, wherein the operational thread
4 and the debugger thread both execute on a common
5 dissimilar processor from the plurality of dissimilar
6 processors.

1 20. The computer program product as described in claim 15
2 wherein the dissimilar processor is a processing unit.